Electronically Filed February 12, 2007

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application of:	Chong-Ming Lin et al.	Confirmation No.:	2040
Serial No.:	10/683,719	Art Unit:	2814
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For:	Structure and Method for Enhanced Performance in Semiconductor Substrates	Attorney Docket No.:	65860-5046-US (formerly LOVO-064)

STATEMENT UNDER 37 C.F.R. § 3.73(b)

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

QSpeed Semiconductor Inc., a corporation, states that it is the assignee of the entire right, title and interest in the patent application/patent identified above by virtue of an assignment from the inventor(s) of the patent application/patent identified above.

A chain of title from the inventor(s), of the patent application/patent identified above, to the current assignee as shown below:

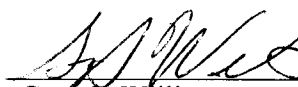
1. From: **Chong Ming Lin, Jay Denning and Ho Yuan Yo**
To: **Lovoltech, Inc.**
The document was recorded in the United States Patent and Trademark Office on May 26, 2004 at Reel 015368, Frame 0157;

2. From: **Lovoltech, Inc.**
To: **QSpeed Semiconductor Inc.**
The document was recorded in the United States Patent and Trademark Office on December 8, 2006 at Reel 018597, Frame 0920; and

3. From: **QSpeed Semiconductor Inc.**
To: **QSpeed Semiconductor Inc.**
The document was recorded in the United States Patent and Trademark Office on December 12, 2006 at Reel 018616, Frame 0298;

The undersigned is authorized to act on behalf of the assignee.

Date: February 12, 2007



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